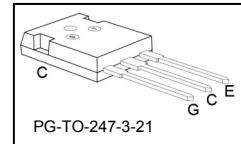
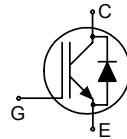


## Low Loss DuoPack : IGBT in TrenchStop® and Fieldstop technology with soft, fast recovery anti-parallel EmCon HE diode

- Very low  $V_{CE(sat)}$  1.5 V (typ.)
- Maximum Junction Temperature 175 °C
- Short circuit withstand time – 5μs
- Designed for :
  - Frequency Converters
  - Uninterrupted Power Supply
- TrenchStop® and Fieldstop technology for 600 V applications offers :
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
  - very high switching speed
- Positive temperature coefficient in  $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel EmCon HE diode
- Qualified according to JEDEC<sup>1</sup> for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	$V_{CE}$	$I_C$	$V_{CE(sat), T_j=25^\circ C}$	$T_{j,max}$	Marking	Package
IKW50N60T	600V	50A	1.5V	175°C	K50T60	PG-TO-247-3-21

### Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	600	V
DC collector current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$	$I_C$	80 <sup>2)</sup> 50	A
Pulsed collector current, $t_p$ limited by $T_{j,max}$	$I_{Cpuls}$	150	
Turn off safe operating area ( $V_{CE} \leq 600V$ , $T_j \leq 175^\circ C$ )	-	150	
Diode forward current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$	$I_F$	100 50	
Diode pulsed current, $t_p$ limited by $T_{j,max}$	$I_{Fpuls}$	150	
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time <sup>3)</sup> $V_{GE} = 15V$ , $V_{CC} \leq 400V$ , $T_j \leq 150^\circ C$	$t_{SC}$	5	μs
Power dissipation $T_C = 25^\circ C$	$P_{tot}$	333	W
Operating junction temperature	$T_j$	-40...+175	°C
Storage temperature	$T_{stg}$	-55...+175	
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

<sup>1</sup> J-STD-020 and JESD-022

<sup>2)</sup> Value limited by bond wire

<sup>3)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction – case	$R_{thJC}$		0.45	K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		0.8	
Thermal resistance, junction – ambient	$R_{thJA}$		40	

**Electrical Characteristic**, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=50\text{A}$	-	1.5	2	
		$T_j=25^\circ\text{C}$	-	1.9	-	
Diode forward voltage	$V_F$	$V_{GE}=0\text{V}, I_F=50\text{A}$	-	1.65	2.05	
		$T_j=25^\circ\text{C}$	-	1.6	-	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=0.8\text{mA}, V_{CE}=V_{GE}$	4.1	4.9	5.7	
		$V_{CE}=600\text{V}, V_{GE}=0\text{V}$	-	-	40	$\mu\text{A}$
Zero gate voltage collector current	$I_{CES}$	$T_j=25^\circ\text{C}$	-	-	1000	
		$T_j=175^\circ\text{C}$	-	-	-	
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE}=20\text{V}, I_C=50\text{A}$	-	31	-	S
Integrated gate resistor	$R_{Gint}$			-	-	$\Omega$

**Dynamic Characteristic**

Input capacitance	$C_{iss}$	$V_{CE}=25\text{V},$	-	3140	-	pF
Output capacitance	$C_{oss}$	$V_{GE}=0\text{V},$	-	200	-	
Reverse transfer capacitance	$C_{rss}$	$f=1\text{MHz}$	-	93	-	
Gate charge	$Q_{\text{Gate}}$	$V_{CC}=480\text{V}, I_C=50\text{A}$	-	310	-	nC
$V_{GE}=15\text{V}$						
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13	-	nH
Short circuit collector current <sup>1)</sup>	$I_{C(SC)}$	$V_{GE}=15\text{V}, t_{SC}\leq 5\mu\text{s}$	-	458.3	-	A
		$V_{CC} = 400\text{V},$				
		$T_j \leq 150^\circ\text{C}$				

<sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Switching Characteristic, Inductive Load, at  $T_j=25^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=50\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=7\ \Omega$ , $L_\sigma^{(1)}=103\text{nH}$ , $C_\sigma^{(1)}=39\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	26	-	ns
Rise time	$t_r$		-	29	-	
Turn-off delay time	$t_{d(off)}$		-	299	-	
Fall time	$t_f$		-	29	-	
Turn-on energy	$E_{on}$		-	1.2	-	mJ
Turn-off energy	$E_{off}$		-	1.4	-	
Total switching energy	$E_{ts}$		-	2.6	-	

**Anti-Parallel Diode Characteristic**

Diode reverse recovery time	$t_{rr}$	$T_j=25^\circ\text{C}$ , $V_R=400\text{V}$ , $I_F=50\text{A}$ , $di_F/dt=1280\text{A}/\mu\text{s}$	-	143	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	1.8	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	27.7	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	671	-	$\text{A}/\mu\text{s}$

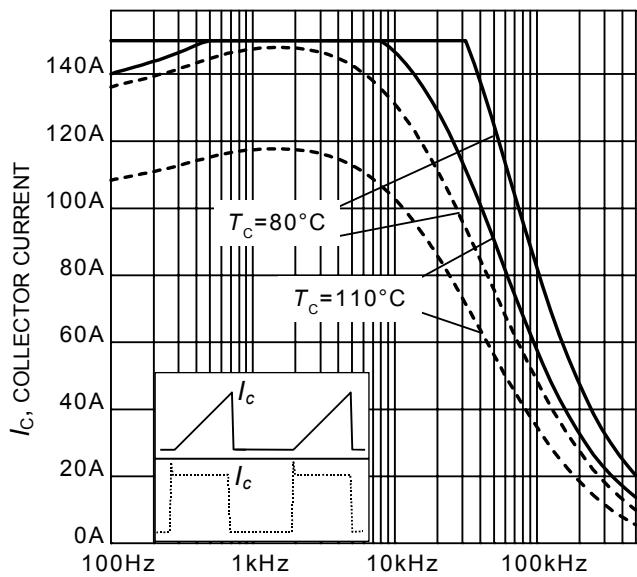
**Switching Characteristic, Inductive Load, at  $T_j=175^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=50\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=7\ \Omega$ , $L_\sigma^{(1)}=103\text{nH}$ , $C_\sigma^{(1)}=39\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	27	-	ns
Rise time	$t_r$		-	33	-	
Turn-off delay time	$t_{d(off)}$		-	341	-	
Fall time	$t_f$		-	55	-	
Turn-on energy	$E_{on}$		-	1.8	-	mJ
Turn-off energy	$E_{off}$		-	1.8	-	
Total switching energy	$E_{ts}$		-	3.6	-	

**Anti-Parallel Diode Characteristic**

Diode reverse recovery time	$t_{rr}$	$T_j=175^\circ\text{C}$ , $V_R=400\text{V}$ , $I_F=50\text{A}$ , $di_F/dt=1280\text{A}/\mu\text{s}$	-	205	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	4.3	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	40.7	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	449	-	$\text{A}/\mu\text{s}$

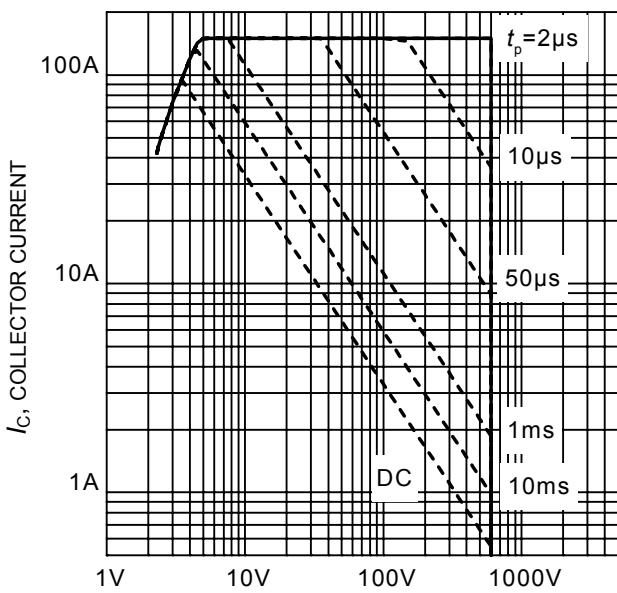
<sup>1)</sup> Leakage inductance  $L_\sigma$  and Stray capacity  $C_\sigma$  due to dynamic test circuit in Figure E.



$f$ , SWITCHING FREQUENCY

**Figure 1. Collector current as a function of switching frequency**

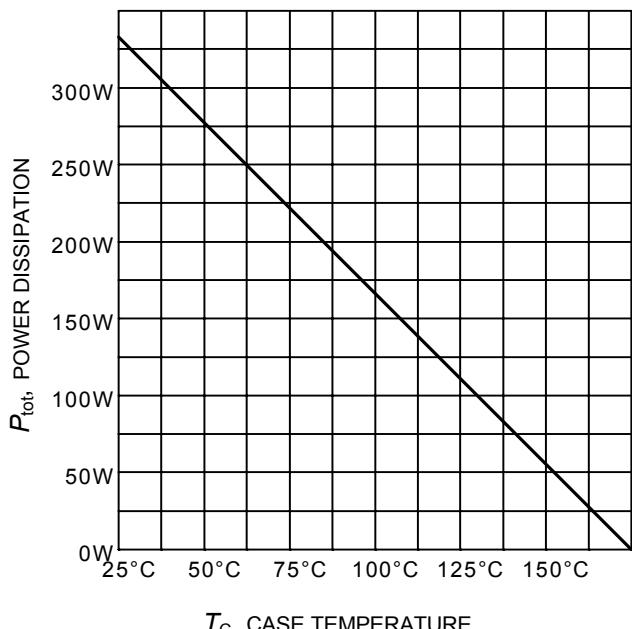
( $T_j \leq 175^\circ\text{C}$ ,  $D = 0.5$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $R_G = 7\Omega$ )



$V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 2. Safe operating area**

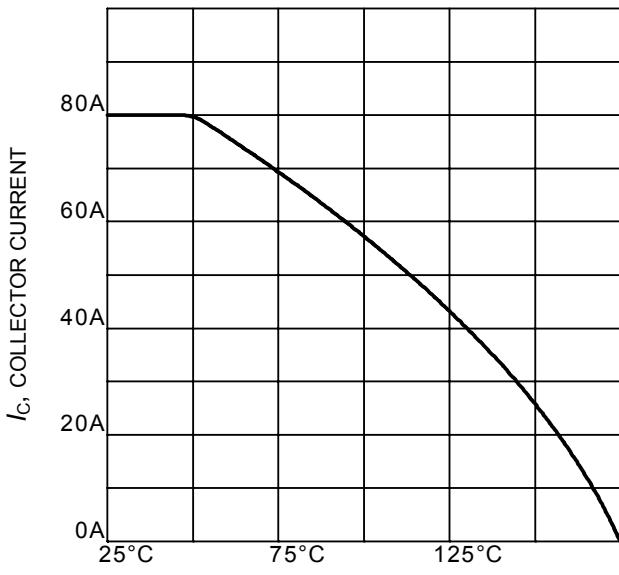
( $D = 0$ ,  $T_C = 25^\circ\text{C}$ ,  $T_j \leq 175^\circ\text{C}$ ,  $V_{GE}=15\text{V}$ )



$T_C$ , CASE TEMPERATURE

**Figure 3. Power dissipation as a function of case temperature**

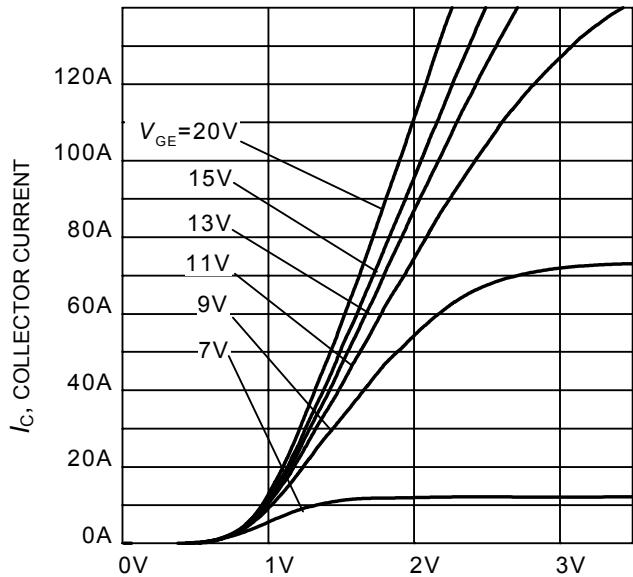
( $T_j \leq 175^\circ\text{C}$ )

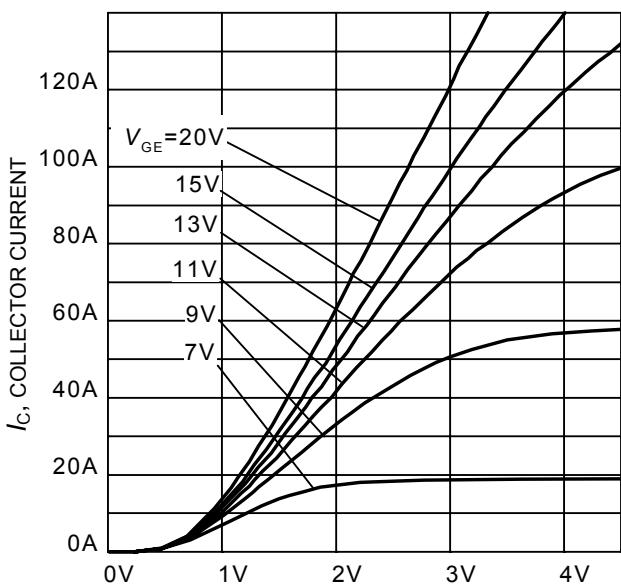


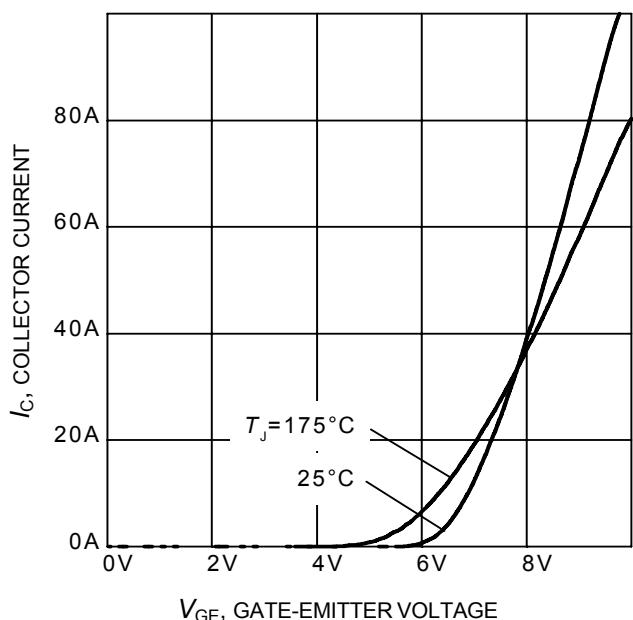
$T_C$ , CASE TEMPERATURE

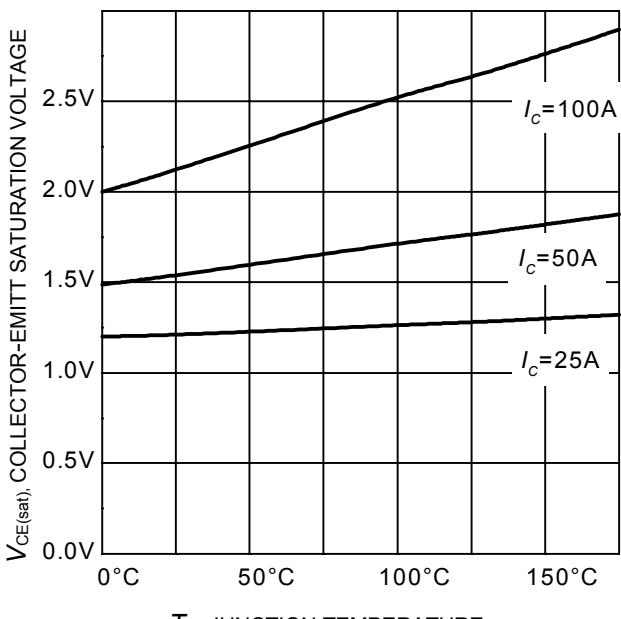
**Figure 4. Collector current as a function of case temperature**

( $V_{GE} \geq 15\text{V}$ ,  $T_j \leq 175^\circ\text{C}$ )

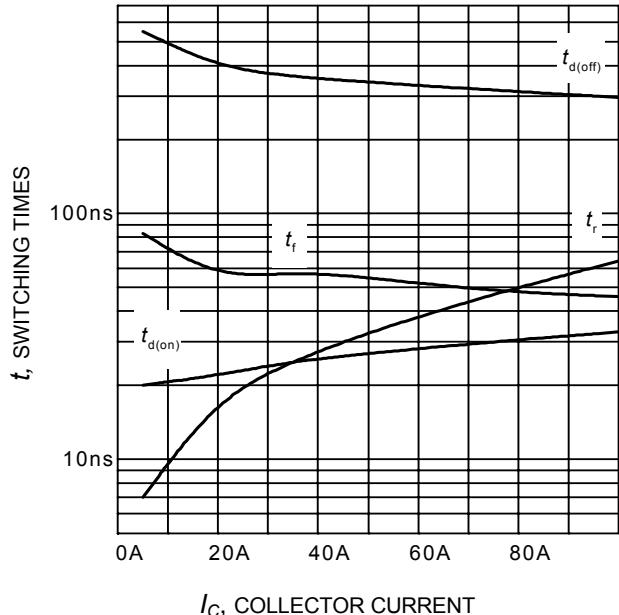

 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 5. Typical output characteristic**  
 $(T_j = 25^\circ\text{C})$ 

 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

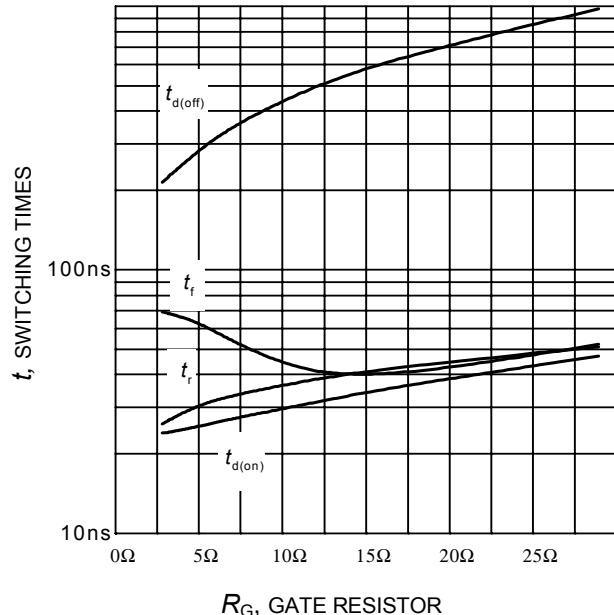
**Figure 6. Typical output characteristic**  
 $(T_j = 175^\circ\text{C})$ 

 $V_{GE}$ , GATE-EMITTER VOLTAGE

**Figure 7. Typical transfer characteristic**  
 $(V_{CE} = 10\text{V})$ 

 $T_j$ , JUNCTION TEMPERATURE

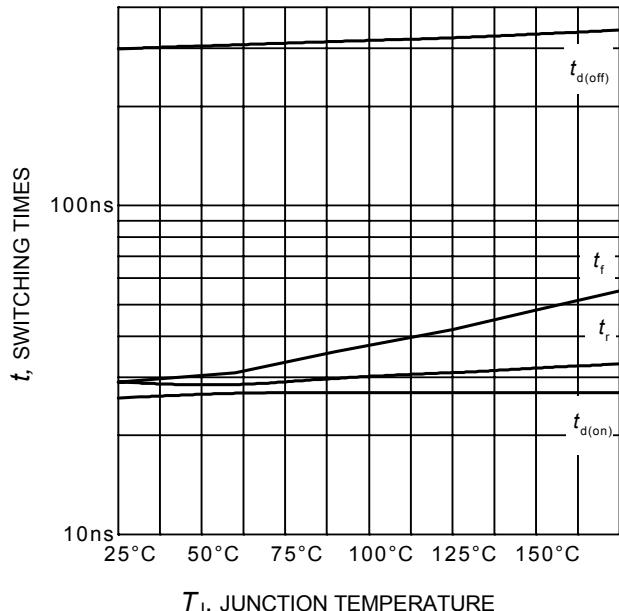
**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
 $(V_{GE} = 15\text{V})$



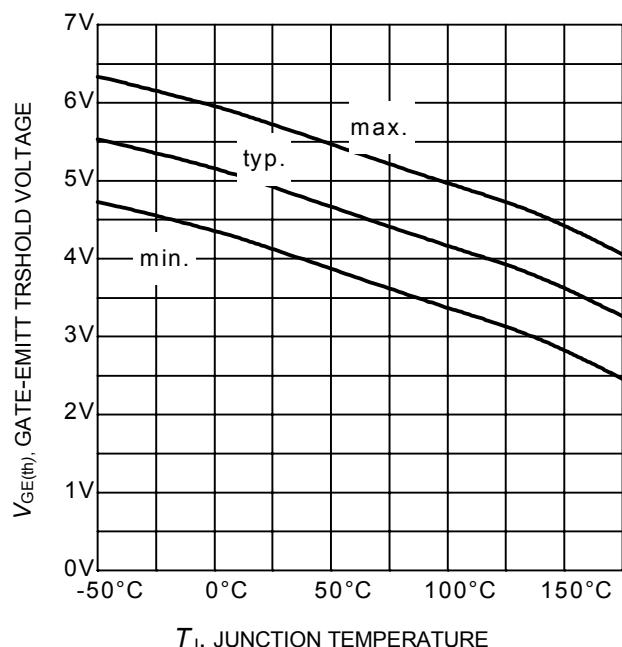
**Figure 9.** Typical switching times as a function of collector current  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $R_G = 7\Omega$ ,  
Dynamic test circuit in Figure E)



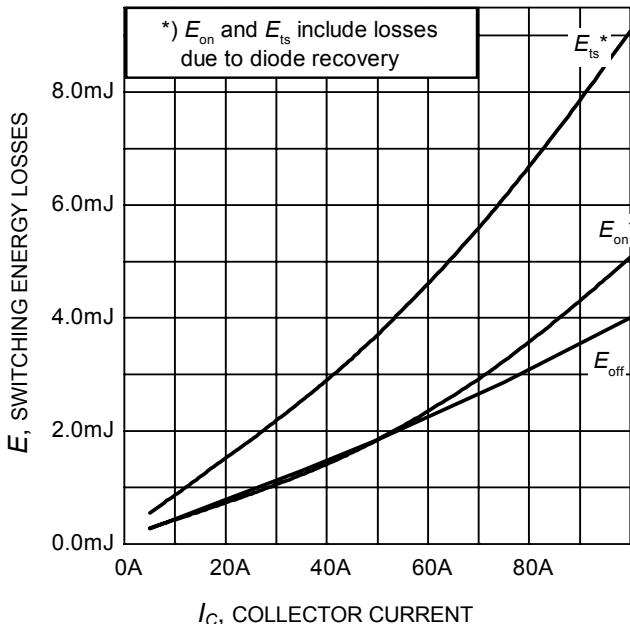
**Figure 10.** Typical switching times as a function of gate resistor  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 50\text{A}$ ,  
Dynamic test circuit in Figure E)



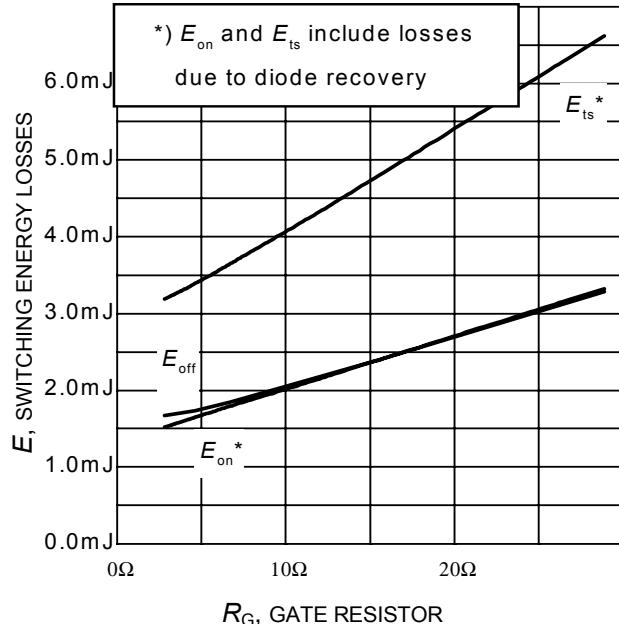
**Figure 11.** Typical switching times as a function of junction temperature  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 50\text{A}$ ,  $R_G = 7\Omega$ ,  
Dynamic test circuit in Figure E)



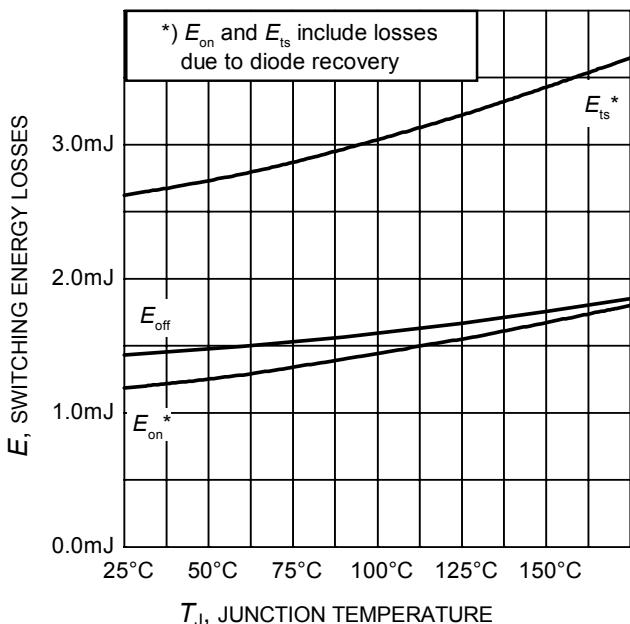
**Figure 12.** Gate-emitter threshold voltage as a function of junction temperature  
( $I_C = 0.8\text{mA}$ )



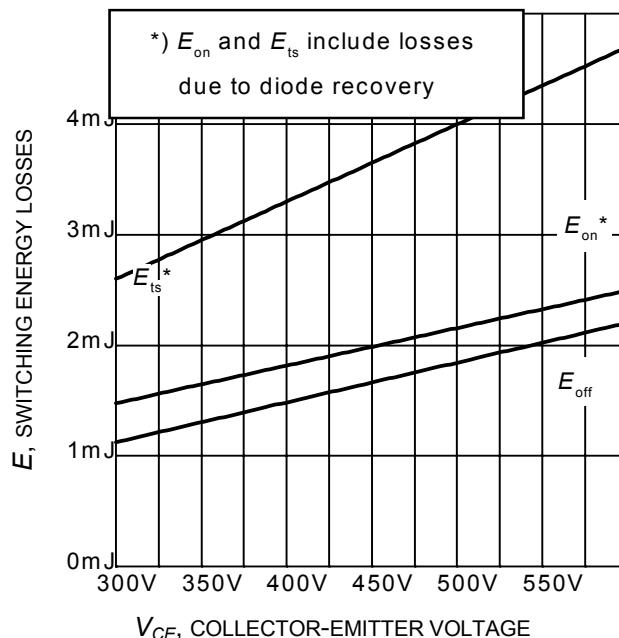
**Figure 13. Typical switching energy losses as a function of collector current**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $R_G = 7\Omega$ ,  
Dynamic test circuit in Figure E)



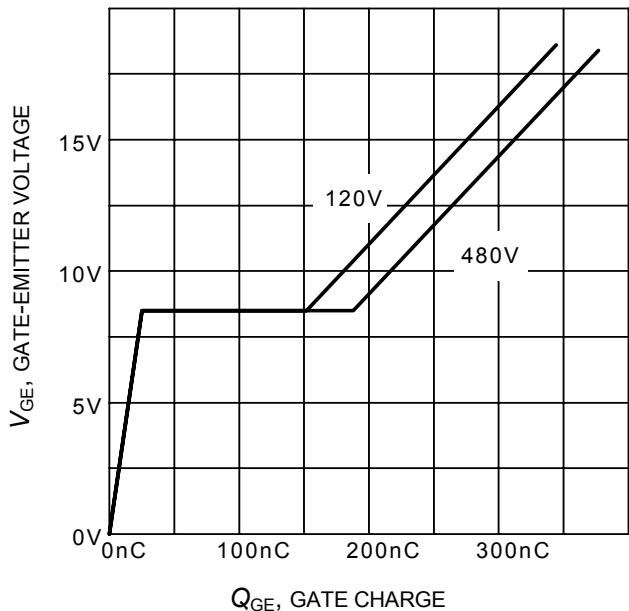
**Figure 14. Typical switching energy losses as a function of gate resistor**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 50\text{A}$ ,  
Dynamic test circuit in Figure E)



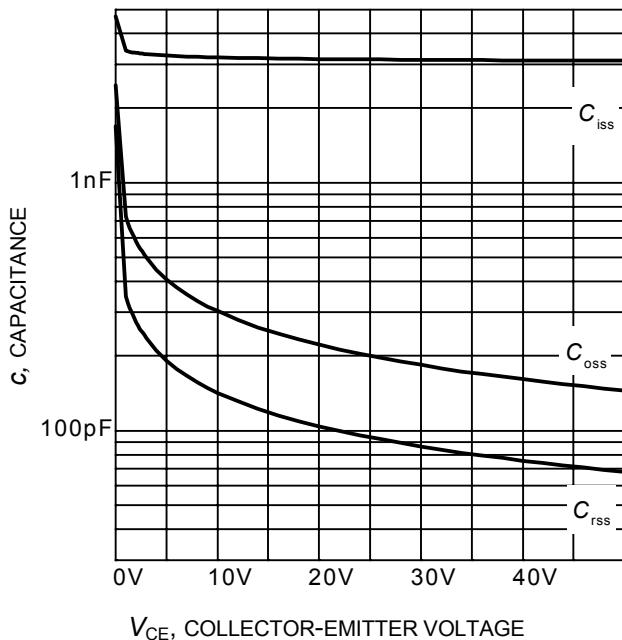
**Figure 15. Typical switching energy losses as a function of junction temperature**  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 50\text{A}$ ,  $R_G = 7\Omega$ ,  
Dynamic test circuit in Figure E)



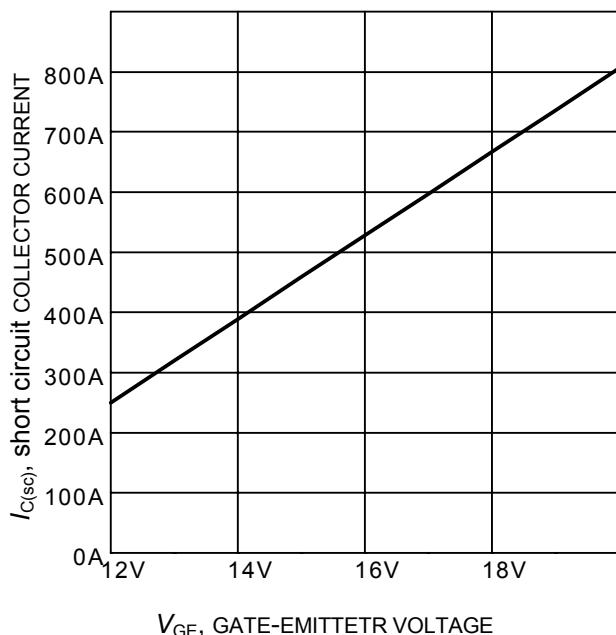
**Figure 16. Typical switching energy losses as a function of collector-emitter voltage**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 50\text{A}$ ,  $R_G = 7\Omega$ ,  
Dynamic test circuit in Figure E)



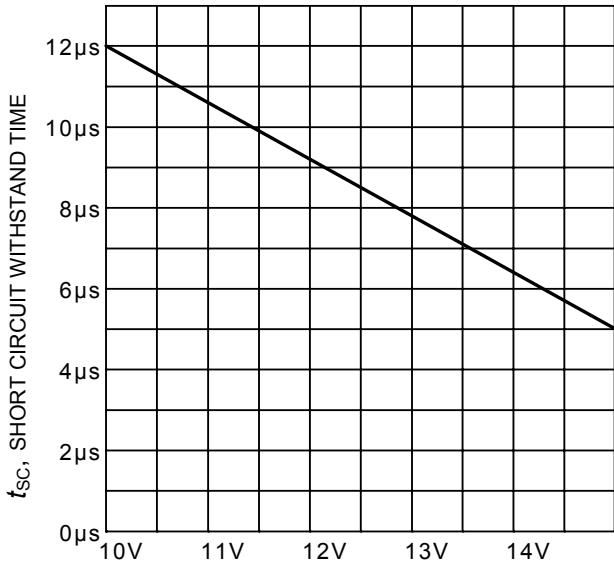
**Figure 17. Typical gate charge**  
( $I_C=50$  A)



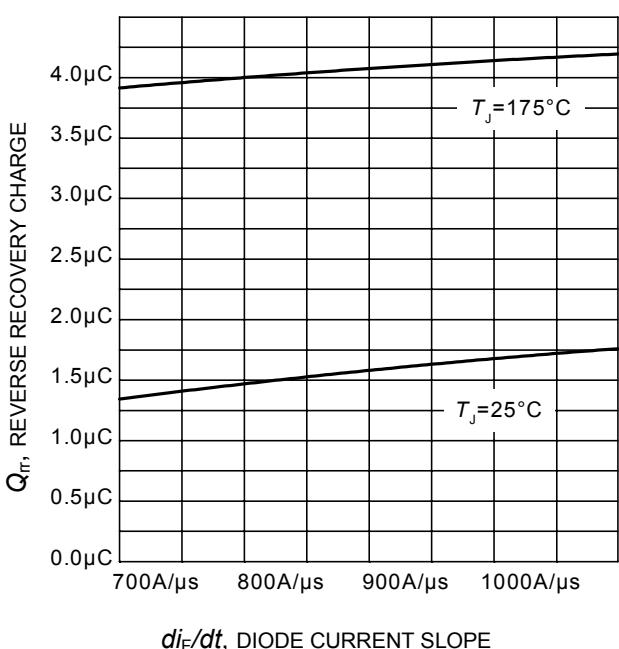
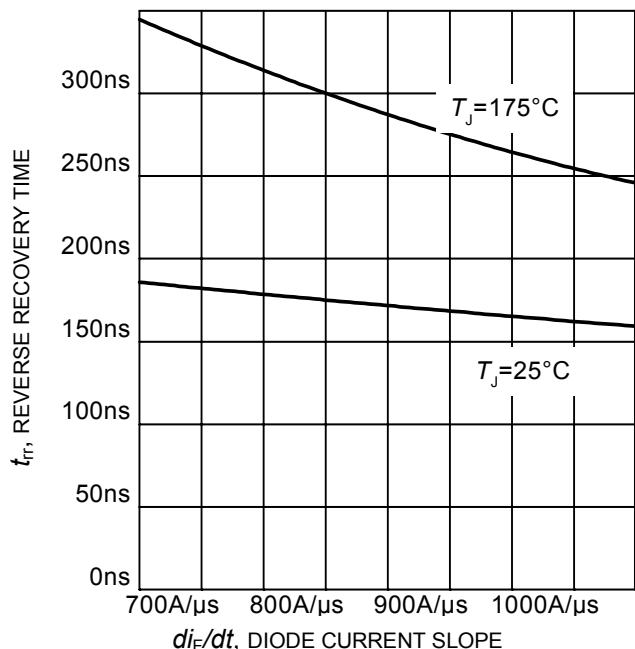
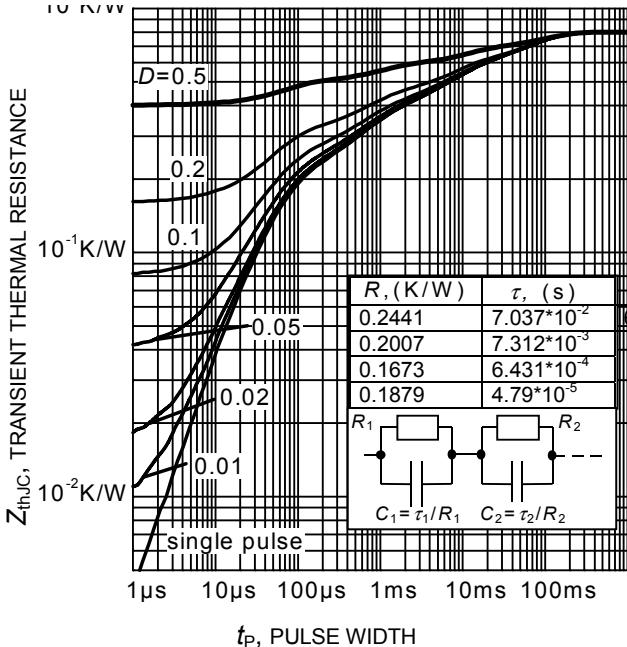
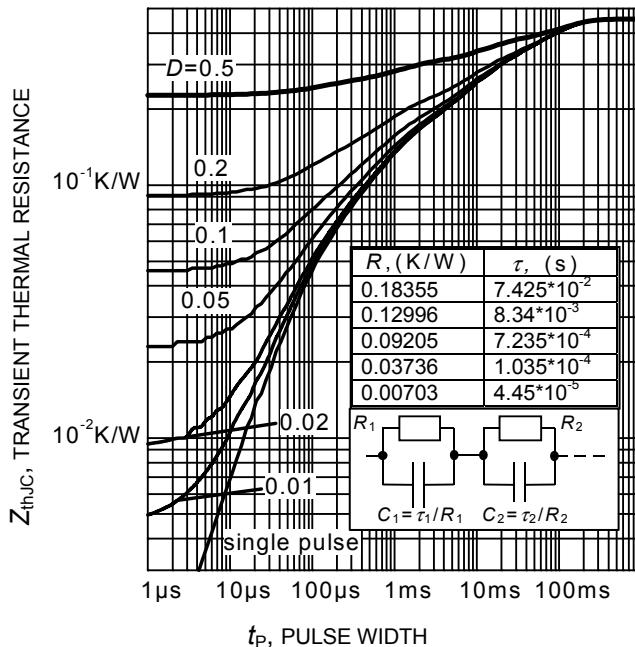
**Figure 18. Typical capacitance as a function**  
of collector-emitter voltage  
( $V_{GE}=0$  V,  $f = 1$  MHz)

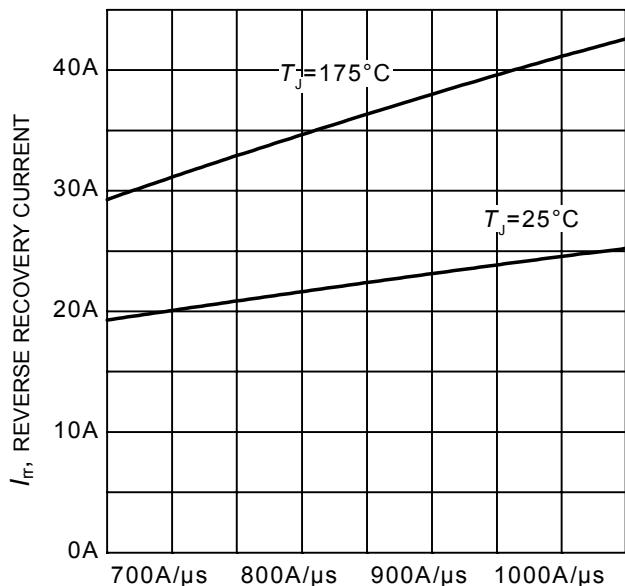


**Figure 19. Typical short circuit collector**  
current as a function of gate-  
emitter voltage  
( $V_{CE} \leq 400$  V,  $T_j \leq 150^\circ\text{C}$ )



**Figure 20. Short circuit withstand time as a**  
function of gate-emitter voltage  
( $V_{CE}=600$  V, start at  $T_j=25^\circ\text{C}$ ,  
 $T_{jmax}<150^\circ\text{C}$ )

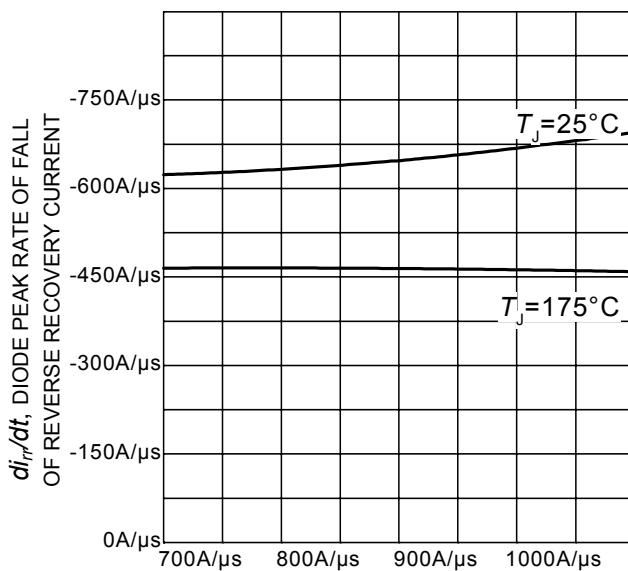




$di_F/dt$ , DIODE CURRENT SLOPE

**Figure 25. Typical reverse recovery current as a function of diode current slope**

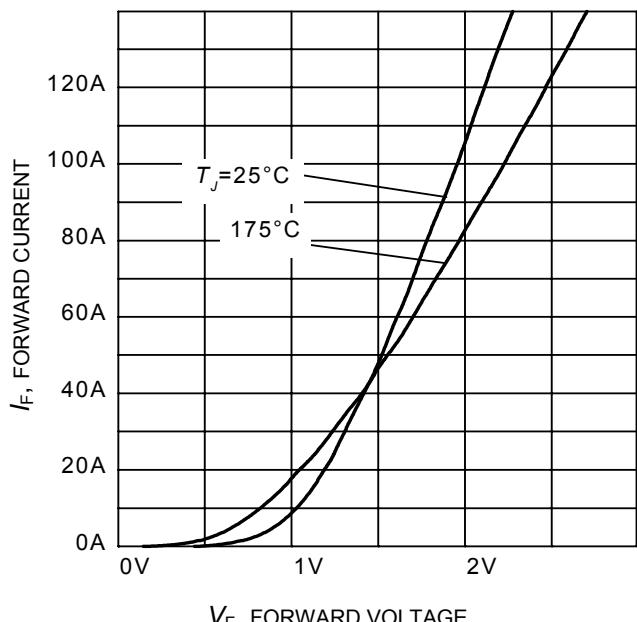
( $V_R = 400\text{V}$ ,  $I_F = 50\text{A}$ ,  
Dynamic test circuit in Figure E)



$di_F/dt$ , DIODE CURRENT SLOPE

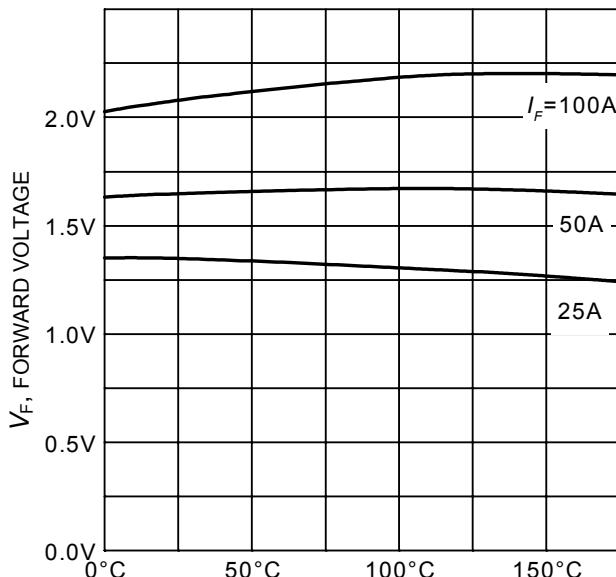
**Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**

( $V_R=400\text{V}$ ,  $I_F=50\text{A}$ ,  
Dynamic test circuit in Figure E)



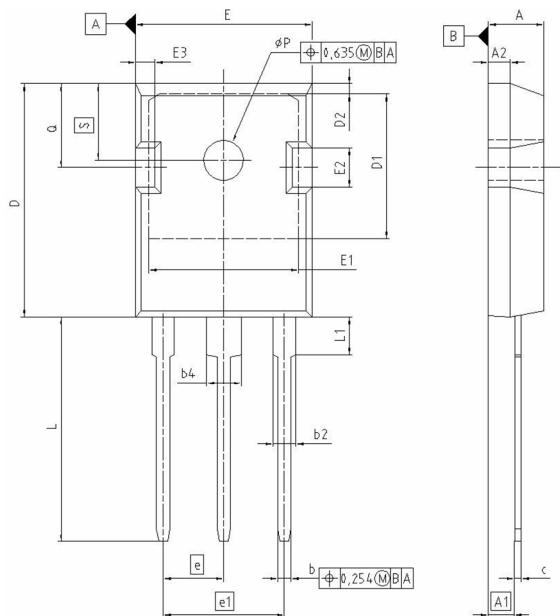
$V_F$ , FORWARD VOLTAGE

**Figure 27. Typical diode forward current as a function of forward voltage**

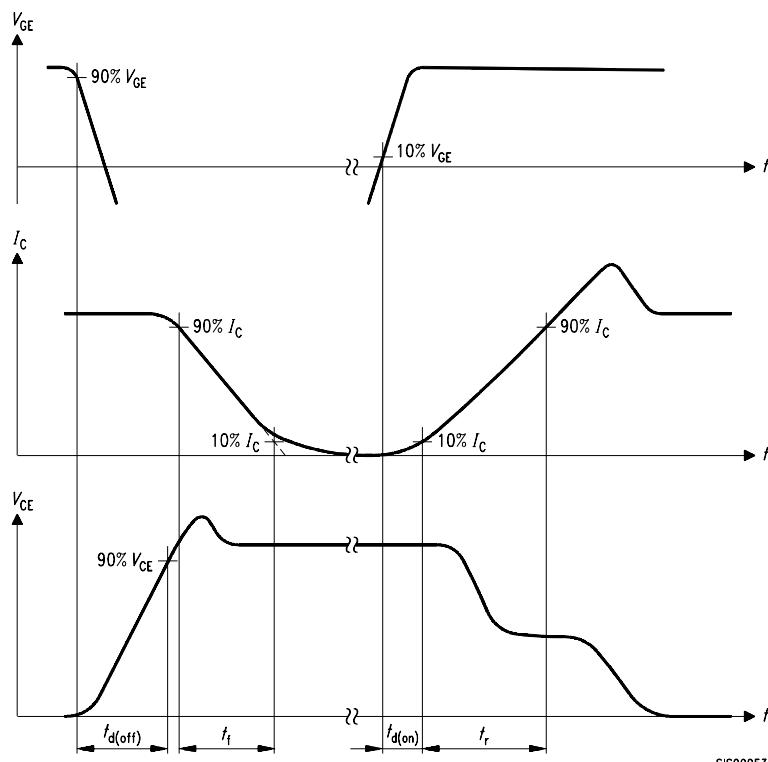
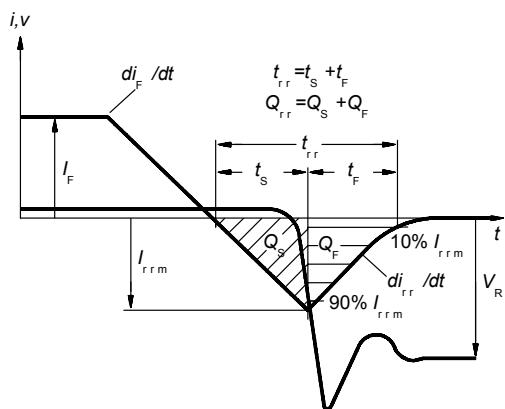
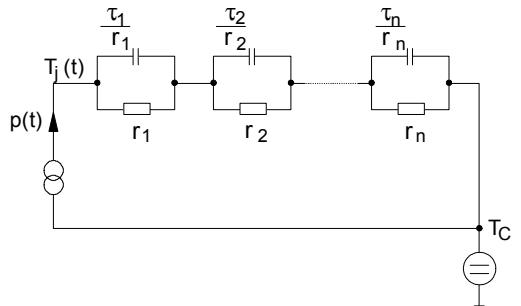
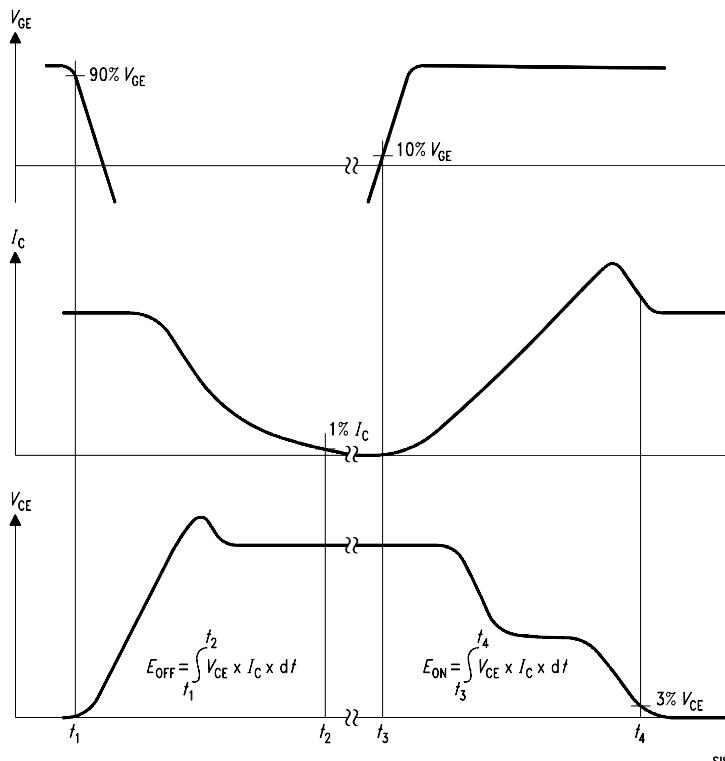
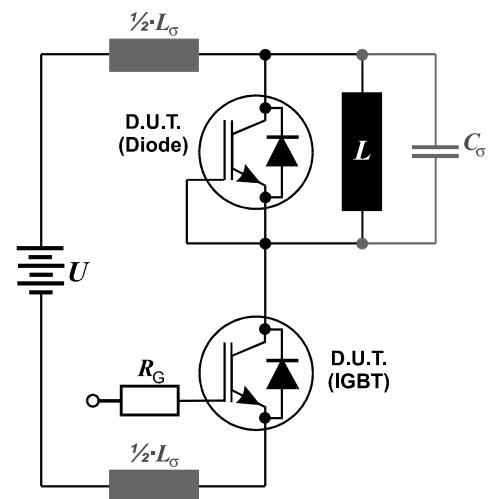


$T_J$ , JUNCTION TEMPERATURE

**Figure 28. Typical diode forward voltage as a function of junction temperature**

**PG-T0247-3-21**


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
<b>A</b>	4.903	5.157	0.193	0.203
<b>A1</b>	2.273	2.527	0.092	0.096
<b>A2</b>	1.853	2.107	0.075	0.081
<b>b</b>	1.073	1.327	0.047	0.052
<b>b2</b>	1.903	2.386	0.075	0.094
<b>b4</b>	2.870	3.454	0.113	0.136
<b>c</b>	0.549	0.752	0.024	0.030
<b>D</b>	20.823	21.077	0.820	0.830
<b>D1</b>	17.323	17.831	0.682	0.702
<b>D2</b>	1.063	1.317	0.042	0.052
<b>E</b>	15.773	16.027	0.621	0.631
<b>E1</b>	13.893	14.147	0.547	0.557
<b>E2</b>	3.683	3.937	0.145	0.155
<b>E3</b>	1.683	1.937	0.066	0.076
<b>e</b>	5.450		0.215	
<b>e1</b>	10.900		0.430	
<b>N</b>	3		3	
<b>L</b>	20.053	20.307	0.789	0.799
<b>L1</b>	4.168	4.472	0.164	0.176
<b>øP</b>	3.559	3.661	0.140	0.144
<b>Q</b>	5.493	5.747	0.216	0.226
<b>S</b>	6.043	6.297	0.238	0.248


**Figure A. Definition of switching times**

**Figure C. Definition of diodes switching characteristics**

**Figure D. Thermal equivalent circuit**

**Figure B. Definition of switching losses**

**Figure E. Dynamic test circuit**

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